

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Daniel Venglarik on 3/22/2010.

The application has been amended as follows:

Please replace all previous claims with attached amended claims, wherein

Claims 2, 3, 6-13 and 16-21 are allowed, and

Claims 1, 4-5 and 14-15 are cancelled.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: The prior art on record fail to teach the combination of elements of independent claims. With respect to claims 2 and 12, the prior art on record fail to teach detecting a load instruction based only on the base and offset values of a previous stored instruction without calculating the effective address (i.e., base address value added to the offset address value), wherein the base value and offset value of the load instruction are identical to each other and are also equivalent to the base and offset values of the store instruction. With respect to claim 2, the prior on record fail to show using the syntax for the first instruction and the syntax for the second instruction to determine a relationship between

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the first memory location and the second memory location, without using the effective address of the first memory location or the effective address of the second memory location to determine the relationship between the first memory location and the second memory location.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to IDRIS N. ALROBAYE whose telephone number is (571)270-1023. The examiner can normally be reached on Mon-Fri from 8:00 to 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on 571-272-4162. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Eddie P Chan/
Supervisory Patent Examiner, Art Unit 2183

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